

Githin George Oommen

Senior Embedded Software Engineer

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Born 09/09/1989 githin89@gmail.com

I'm an Indian citizen and holding UAE residence VISA. My notice period is 90 days.



TECHNICAL SKILLS

C (8 years of project experience) | C++ (2) | C# (1) | Python (1) | STM32 (5) | Windows (8) | GIT (2) |

IBM Rational DOORS (5) | Lauterbach Debugger (3) | SharcC (5) | IBM ALM (5) | Embedded Automotive (5) | CAN (5) | I2C (2) | SPI (2) | UART (2) | Device Driver (5) | AUTOSAR BSW (3) | CANALYSER (3) | CANOE (3) | ASPICE (1) | Matlab (2) | Simulink (2) | Raspberry Pi (2) | Arduino (2) |

SPI (5) | I2C (5) | UART (2) | RTOS (2) | Wi-Fi (2) | ZigBee (2) | GSM (2) | Embedded Linux (4) | HomeAssistant (2) | VS Code (2) | BLDC, Stepper & Servo Motors (2) | QC (5) | Git (4) | Jira (2) | Scrum & Agile Methodology (5).

WORK EXPERIENCE

08/2017 – today

Automotive Embedded Software Engineer

Bosch, Bangalore, India

Project: Vehicle active safety product (ECU) - Software that offers ABS, ESP, Auto Hold, Hill Hold and many features for passenger cars used by clients such as Volvo, Jaguar Land Rover or General Motors e.g.

- Leading a scrum team of 10, developing the features for ABS and ESP ECU for passenger cars. Developed the device driver MCAL for the Autosar's base layer with BCT tool and embedded C.

- Conducting customer requirement analysis using IBM Rational DOORS. Defecting reduction with problem-solving techniques (CAR and 8D analysis).

- Developing the base layer for sensors and actuators for the implementation of ABS and ESP in C.

- Software development for customer-specific features and integration of platform software modules. Reproduce and analyze internal defects and field issues. Debugging the software with a lauterbach debugger.

- Personally ensured the ASIL Rating through functional safety testing and maintaining the rating over 80% of success.

Technologies used: BCT | SharcC | IBM Rational DOORS | QC | Debugger | HIL | SIL

11/2016 – 07/2017

Software Engineer

HCL, Bangalore, India

Project: Smart home lighting application for offices used by companies such as Intel. Part of an agile team of 5 responsible for analyzing the customer requirements and preparation of system-level and

software-level design documents. Developed a software in C for the streetlight and smart offices lighting to avoid human intervention in the turning on of the illumination system. Involved in the selection of microcontroller port and design of electronic circuit diagram on Orcad and converted the project in PCB for the mechanical design.

Technologies used: Sharc | Visio | Proteus | Orcad.

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08/2014 – 11/2016 **Software Engineer**

IgrenEnergi Inc, Bangalore, India

Project: Solar based product which is targeted for the Indian and US market. Responsible for developing a Solar Product “DC Optimizer” which is used to enhance the solar panel power and deliver the maximum power to the load.

- Part of a scrum team of 10, involved in the software development for the DC optimizer.
- Developed the firmware on C for the optimizer to follow MPPT. Personally created algorithm in the firmware. Developed protection actions for the circuit depending on the voltage readings by the ADC's. Interfaced the optimizer with different inverters (3 and 5KW).
- This system enhanced 80/90% more the production of power generated by solar panels.

Technologies used: C | Sharc | Visio | Proteus | Orcad | DC oscilloscope |

EDUCATION

07/2012 – 05/2014 Master Of Technology, Embedded Systems
Sathyabama Institute of Science and Technology, Chennai, India

07/2007 - 09/2011 Bachelor of Technology, Electronics and Communication
Cochin University of Science and Technology

LANGUAGE SKILLS

English (C1) | Hindi | Malayalam (Mother tongue)

HOBBIES

Bike Riding | Playing Badminton

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